Patent Application Number: 10/751,562 Attorney Docket Number: GC.7027

In the Specification

Please amend paragraphs [0018]-[0020] as follows:

[0018] As a simple example, assuming the outputs are added in the analog domain with the following formula,

$$V_{OUT} = 0.9(V_{OUT1} + V_{OUT2})$$

[0019] and also assuming

$$T_{IN1} = 9T_{IN2}$$
,

[0020] the illumination intensity versus the output voltage relation in Figure <u>5</u>17 is obtained. The dotted line extension of the first segment indicates the output of a conventional pixel with a single integration time.

$$T_{IN} = T_{IN1} + T_{IN2}$$